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## PATENT ABSTRACTS OF JAPAN

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(21) Application number: **05184251**(71) Applicant: **SONY CORP**(22) Date of filing: **26.07.93**(72) Inventor: **IKEDA YASUNARI**(54) **DIGITAL DEMODULATOR**

## (57) Abstract:

**PURPOSE:** To provide the digital modulator and demodulator for orthogonal frequency modulation in which an accurate phase of a DFT time window signal is obtained.

**CONSTITUTION:** A discrete Fourier transformation circuit 121 applies discrete Fourier transformation to a received orthogonal frequency multiplex signal to demodulate the signal. A discrete Fourier transformation circuit 171 is used to exclusively generate a time window signal and has the similar function to that of the discrete Fourier transformation circuit 121. A time window signal generating circuit 174 generates a time window signal DFT 1 to the discrete Fourier transformation circuit 121 and a time window signal DFT2 to the discrete Fourier transformation circuit 171 based on the transformation result of the discrete Fourier transformation circuit 171. The time window signal generating circuit 174 always obtains the optimum phase of the time window signal by changing the phase of the time window signal DFT1 to match the phase to that of the time window signal DFT2 when the phase of the time window signal DFT1 is deviated from the proper phase to

keep the phase of the signal without giving an undesired phase change to the time window signal DFT1.

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